

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Emil Lambrache et al.

PATENT APPLICATION

Serial No.: 10/722,819

Group Art Unit: 2182

Filed: November 25, 2003

Examiner:

For: SERIAL PERIPHERAL INTERFACE (SPI) APPARATUS WITH

WRITE BUFFER FOR IMPROVING DATA THROUGHPUT

Supplemental Information Disclosure Statement

Hon. Commissioner for Patents Alexandria, VA 22313

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.56. A copy of the reference is enclosed.

Other Reference

Copy of Section 8 "Synchronous Serial Peripheral Interface", pgs. 291-316, of Motorola, "M68HC11" Reference Manual, Rev. 6, 4/2002.

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail envelope oner for addressed Commissioner Patents, Alexandria, 22313

Signed:

Typed Name:

Sally Azevedo

March 9, 2004

Respectfully submitted,

Thomas Schneck

Req. No. 24,518

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					Applicant: Emil Lambrache et al.					
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.